A Case Study on Context Maintenance in Dynamic Hybrid Race Detectors

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Abstract—Many dynamic hybrid race detectors aim at detecting violations of the lockset discipline in execution traces of multithreaded programs. They are designed to abstract memory accesses appearing in traces as contexts. Nonetheless, they keep these contexts in different extents and partition the sets of contexts into equivalent classes of different granularity. In our case study, we compare three detectors using the PARSEC benchmark suite to examine the impact of using unrestricted strategy or restricted strategy for keeping these contexts in sequence on detection effectiveness, and the impact of partitioning context sequences by different granularities on scalability in time cost. The case study results indicate that using restricted context sequences sufficed to detect very high proportions of locking discipline violations detectable by using unrestricted context sequences, and the partitioning of context sets into finer equivalent classes significantly lowers the scalability in time cost with increasing number of threads to handle the same input workload.

Keyword—Multithreaded program, data race, concurrency bug detection, dynamic analysis, case study

I. INTRODUCTION

A data race is a concurrency bug that may appear in an execution trace of a multithreaded program. It occurs when two threads in the same trace access the same shared memory location without any protection by synchronization operations, and at least one of these accesses is a write [6]. Its occurrence may result in program state corruption or produce a crashing run. Previous case studies on open-source multithreaded programs show that the presence of a data race often indicates the presence of other higher-level concurrency bugs, such as atomicity violation or linearizability problem, in the same program [2][13].

Dynamic race detection techniques track events (i.e., memory accesses and synchronization operations) performed by various threads in execution traces. Most of them analyze these events and their ordering on-the-fly. These on-the-fly race detectors can be further classified into happens-before (HB) based detectors [6][9], locking discipline (LD) based detectors [17], and hybrid detectors [10][15][22][23][24].

The HB relation [11] is a causal order of events in a trace, and HB based detectors deem any two accesses to the same shared memory location (and at least one of them being a write) failing to be separated by such a relation over synchronization operations as a data race. The locking discipline (LD) [17] is a heuristic rule that every access to the same shared memory location should be protected by some common lock (i.e., mutex), and detectors based on LD use the notion of lockset to check if the set of common locks being held by different threads along a trace when accessing the same shared memory location becomes empty [17].

Hybrid detectors [10][15][22][23][24] combine both HB based and LD based techniques. Different emphases and strategies result in techniques with different scope of memory accesses to check for race occurrences, memory cost, time efficiency, and scalability with increasing number of threads. To support the above checking, these techniques commonly abstract each past access event as a context, which is a tuple of information extracted from the event. The warning reported by such detectors is referred to as $\emptyset$-race.

In this paper, we present and compare three hybrid detectors on the PARSEC benchmark suite [1] to examine (1) the impact of using unrestricted strategy or restricted strategy for keeping context sequences on detection effectiveness, and (2) the impact of partitioning context sequences by different granularities on scalability in time cost, through two research questions. Our experiment includes three latest hybrid race detectors that detect $\emptyset$-races: AccuLock [19][20], MultiLock-HB [20], and HistLock [21]. Each detector homogenously keeps a past event as a pair of epoch [6] and lockset [17] (i.e., context). Both AccuLock and HistLock keep sequences of such contexts with length restriction; whereas MultiLock-HB has no such restriction. All three detectors set different granularities on partitioning the sets of contexts into equivalent classes (context partitioning for short). In our case study, we have studied whether context sequence with length restriction can compete well with context sequence without such length restriction in terms of detection effectiveness, and whether using a finer granularity on context partitioning is scalable enough compared to using a coarser one.

The main contribution of this paper is twofold: (1) This paper reports a case study to examine both context length restriction and context partitioning granularity as design...
A trace is a sequence of events. Each event has a specific type, which can be a memory access or a synchronization operation. The type of memory accesses is further divided into two sub-types: \(\text{write}(x)\) and \(\text{read}(x)\), denoting to update and read the shared memory location \(x\), respectively.

A synchronization operation is one of the following types: \(\text{fork}(t, u)\), \(\text{join}(u, t)\), \(\text{acquire}(m)\), \(\text{release}(m)\), \(\text{wait}(cv)\), \(\text{signal}(cv)\), \(\text{barrier}_{entry}(cv)\), and \(\text{barrier}_{exit}(cv)\). Operation \(\text{fork}(t, u)\) models that thread \(t\) forks a new thread \(u\). Operation \(\text{join}(u, t)\) models that thread \(u\) joins thread \(t\). Operations \(\text{acquire}(m)\) and \(\text{release}(m)\) model the lock acquisition and release operations that a thread acquires lock \(m\), and releases \(m\). Operation \(\text{wait}(cv)\) models that a thread is blocked until it receives a signal of the conditional variable \(cv\). Operation \(\text{signal}(cv)\) models that a thread sends a signal to each thread waiting on the conditional variable \(cv\). Operations \(\text{barrier}_{entry}(cv)\) and \(\text{barrier}_{exit}(cv)\) model the barrier operations that a thread enters a barrier denoted by the conditional variable \(cv\), and exits the barrier when all the threads required by \(cv\) has arrived at the barrier.

B. Happens-Before Relation and mhb Relation

The happens-before relation is a causal order over the events in a trace which satisfies one of the following three conditions and denoted by \(\rightarrow_{\text{hb}}\). (1) [Program Order]: If \(\alpha\) and \(\beta\) are events performed by the same thread and \(\alpha\) precedes \(\beta\), then \(\alpha \rightarrow_{\text{hb}} \beta\). (2) [Synchronization Order]: If \(\alpha\) and \(\beta\) are synchronization operations from two different threads and \(\alpha\) precedes \(\beta\), then \(\alpha \rightarrow_{\text{hb}} \beta\). (3) [Transitivity]: If \(\alpha \rightarrow_{\text{hb}} \beta\) and \(\beta \rightarrow_{\text{hb}} \gamma\), then \(\alpha \rightarrow_{\text{hb}} \gamma\).

A relaxation of the happens-before relation is to restrict the types of synchronization operations captured via Rule 2 [Synchronization Order] by only considering the operations \(\text{fork}(t, u)\), \(\text{join}(u, t)\), \(\text{wait}(cv)\), \(\text{signal}(cv)\), \(\text{barrier}_{entry}(cv)\), and \(\text{barrier}_{exit}(cv)\), which is popularly referred to as the must-happen-before relation [20][21] or the mhb relation for short, denoted as \(\rightarrow_{\text{mhb}}\). If two events \(\alpha\) and \(\beta\) that are neither \(\alpha \rightarrow_{\text{mhb}} \beta\) nor \(\beta \rightarrow_{\text{mhb}} \alpha\), then the two events are called mhb-concurrent.

C. LD-Based Detection and \(\emptyset\)-Race

Locking discipline (LD) violation is a heuristic that asserts all the accesses to the same shared memory location being protected by at least one lock in common. Specifically, on every event accessing on memory location \(x\) by thread \(t\), the original lockset of \(x\), denoted by \(CL_x\), is updated to the intersection \(CL_x \cap L_t\), where \(L_t\) denotes the set of locks currently held by thread \(t\). If this updated \(CL_x\) is empty (i.e., \(CL_x = \emptyset\)), a LD violation is said to occur.

Definition 1 (\(\emptyset\)-Race): Suppose \(\alpha\) and \(\beta\) are two memory accesses on the memory location \(x\) in a trace with neither \(\alpha \rightarrow_{\text{mhb}} \beta\) nor \(\beta \rightarrow_{\text{mhb}} \alpha\), and \(\alpha\) precedes \(\beta\) in the trace. An \(\emptyset\)-Race involving \(\alpha\) and \(\beta\) is said to occur if and only if at least one of \(\alpha\) and \(\beta\) is a write and a LD violation occurs on \(\beta\).

D. Vector Clock and Epoch

A vector clock (VC) [14] is a tuple of timestamps (i.e., integers) to record a clock for an entity (e.g., thread). We refer to the vector clock being currently held by a thread \(t\) as \(C_t\). It is popular to use VCs to track the causal orders (e.g., \(\rightarrow_{\text{hb}}\) and \(\rightarrow_{\text{mhb}}\)) over the events in a trace.

To alleviate the overhead of some VC comparisons, it can refer to a pair of a clock \(c\) and a thread \(t\) as an epoch [6], denoted as \(c@t\), where \(c = C_t\) (\(C_t\) is the VC of thread \(t\)). For ease of our presentation, we use the notation \(\text{epoch}(t)\) to refer the current epoch of thread \(t\), and we use the notation \(c@t \neweq C_t\) to indicate \(c \leq C_t\) (or \(\neq\) otherwise).

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**Algorithm 1: Read \([\text{AccuLock}]\):** thread \(t\) reads variable \(x\):

1. \(W_x.\text{epoch} \neq \text{epoch}(t) \land R_x[t].\text{epoch} \neq \text{epoch}(t)\) then
2. \(R_x[t].\text{epoch} := \text{epoch}(t)\)
3. \(R_x[t].\text{lockset} := L_t\)
4. \(\text{if } W_x.\text{epoch} \neweq C_t \land R_x[t].\text{lockset} \cap W_x.\text{lockset} = \emptyset\) then
5. \(\text{report a write-read race warning}\)
6. \(\text{end if}\)
7. \(\text{end if}\)

**Algorithm 2: Write \([\text{AccuLock}]\):** thread \(t\) writes variable \(x\):

1. \(W_x.\text{epoch} \neq \text{epoch}(t)\) then
2. \(W_x.\text{epoch} \neweq C_t\) then
3. \(W_x.\text{lockset} := W_x.\text{lockset} \cap L_t\)
4. \(\text{if } W_x.\text{lockset} = \emptyset\) then
5. \(\text{report a write-write race warning}\)
6. \(\text{end if}\)
7. \(\text{else}\)
8. \(W_x.\text{lockset} := L_t\)
9. \(\text{end if}\)
10. \(W_x.\text{epoch} := \text{epoch}(t)\)
11. \(\text{for each thread } s \text{ in read map } R, \text{ do}\)
12. \(\text{if } R[s].\text{epoch} \neq C_t \land R[s].\text{lockset} \cap L_t = \emptyset\) then
13. \(\text{report a read-write race warning}\)
14. \(\text{end if}\)
15. \(\text{end for}\)
16. \(R_t := \emptyset\)
17. \(\text{end if}\)
**Algorithm 3: Read** [MultiLock-HB]: thread $t$ reads variable $x$:

1. update_on_read($R_x[t]$, $W_x[t]$) (update and remove redundant reads)
2. for each thread $u$ in write map $W_x$
3. for each $<$epoch$_u$, lockset$_u$> $\in W_x[u]$ do
4. if epoch$_u \notin C_t \land lockset$_u \cap L_t = \emptyset$ then
5. report a write-read race warning
6. end if
7. end for
8. end for

**Algorithm 4: Write** [MultiLock-HB]: thread $t$ writes variable $x$:

1. update_on_write($W_x[t]$, $R_x[t]$) (update and remove redundant writes)
2. for each thread $u$ in write list $W_x$
3. for each $<$epoch$_u$, lockset$_u$> $\in W_x[u]$ do
4. if epoch$_u \notin C_t \land lockset$_u \cap L_t = \emptyset$ then
5. report a write-write race warning
6. end if
7. end for
8. end for
9. for each thread $u$ in read list $R_x$
10. for each $<$epoch$_u$, lockset$_u$> $\in R_x[u]$ do
11. if epoch$_u \notin C_t \land lockset$_u \cap L_t = \emptyset$ then
12. report a read-write race warning
13. end if
14. end for
15. end for

**Algorithm 5: Read** [HistLock]: thread $t$ reads variable $x$:

1. lastRead := $R_x.last$
2. lastWrite := $W_x.last$
3. if lastRead.epoch $\neq$ epoch(t) $\land$ lastWrite.epoch $\neq$ epoch(t) then
4. for each $<$epoch$_u$, lockset$_u$> $\in W_x$ do
5. if epoch$_u \notin C_t \land lockset$_u \cap L_t = \emptyset$ then
6. report a write-read race warning
7. end if
8. end for
9. $R_x.addLast($epoch(t), lockset$)$
10. if $|R_x| > 0$ then
11. remove $R_x.first$
12. end if
13. end if

**Algorithm 6: Write** [HistLock]: thread $t$ writes variable $x$:

1. lastWrite := $W_x.last$
2. if lastWrite.epoch $\neq$ epoch(t) then
3. for each $<$epoch$_u$, lockset$_u$> $\in W_x$ do
4. if epoch$_u \notin C_t \land lockset$_u \cap L_t = \emptyset$ then
5. report a write-write race warning
6. end if
7. end for
8. for each $<$epoch$_u$, lockset$_u$> $\in R_x$ do
9. if epoch$_u \notin C_t \land lockset$_u \cap L_t = \emptyset$ then
10. report a read-write race warning
11. end if
12. end for
13. $W_x.addLast($epoch(t), lockset$)$
14. if $|W_x| > 0$ then
15. remove $W_x.first$
16. end if
17. end if

**E. Non-Interesting Memory Access w.r.t. $\emptyset$-race**

Most existing race detectors (e.g., [6][20][22][23]) only report one racy pair for a certain data race location, and regard any other (subsequent) accesses to the same racy memory location in the trace as non-interesting. A non-interesting access is also called a redundant access in [20].

Definition 2 (Non-interesting Memory Accesses). Let $a$ and $b$ be two accesses to a shared memory location $x$ made by thread $t_1$, and $c$ be a third access to $x$ made by a different thread $t_2$ such that $a$ precedes $b$ as well as $b$ precedes $c$ in the trace. The access $b$ is said to be non-interesting with respect to $a$, if $a$ and $c$ involves in a $\emptyset$-race whenever $b$ and $c$ involves in a $\emptyset$-race.

**F. Hybrid Detector 1: AccuLock**

AccuLock [19][20] combines the notions of lockset and epoch-based happens-before for race detection. It models the state of a memory access by a couple ($\langle$epoch, lockset$\rangle$). In such a couple, epoch indicates the clock of the corresponding thread to perform the memory access; lockset records the set of locks held by the thread to protect the shared memory location for this memory access. For each shared memory location, AccuLock records one ($\langle$epoch, lockset$\rangle$) instance for the immediate past write performed by any thread (denoted as $W_x$) and a list of ($\langle$epoch, lockset$\rangle$) instances for the immediate past reads performed by each thread (denoted as $R_x$). We refer to $R_x[t]$ as the ($\langle$epoch, lockset$\rangle$) of the immediate past read performed by thread $t$. The main algorithms of AccuLock are shown in Algorithms 1 and 2.

**G. Hybrid Detector 2: MultiLock-HB**

For each shared memory location, MultiLock-HB [20] records two lists of sequences of ($\langle$epoch, lockset$\rangle$) for every thread’s past reads and writes, denoted as $R_x$ and $W_x$, respectively. We refer to $R_x[t]$ and $W_x[t]$ as the sequences of ($\langle$epoch, lockset$\rangle$) of past reads and writes performed by thread $t$, respectively. Rather than keeping all encountered memory accesses, MultiLock-HB checks whether the current access is non-interesting by walking through the corresponding sequence $S$ of past memory accesses.

Both AccuLock and MultiLock-HB are proposed in [20]. To avoid presenting repetitive information, we only show the race checking in the MultiLock-HB’s main algorithms listed in Algorithms 3 and 4.

**H. Hybrid Detector 3: HistLock**

HistLock [21] records ($\langle$epoch, lockset$\rangle$) for each memory access, and keeps ($\langle$epoch, lockset$\rangle$) instances into either the read sequence $R_x$ or the write sequence $W_x$, depending on the
type of memory access (i.e. read and write). Unlike MultiLock-HB, HistLock only uses a single such sequence for all writes performed by all the threads and a single such sequence for all reads performed by all the threads. When thread \( t \) reads or writes a shared memory location \( x \), if the \( \text{epochs} \) of the last (\( \text{epoch} \), lockset) in the sequences \( R_x \) and \( W_x \) (or only \( W_x \) for writes) are different from the epoch of the current read or write, HistLock adds the (\( \text{epoch} \), lockset) of the current memory access into \( R_x \) or \( W_x \) (for reads or writes, respectively). If the number of elements in \( R_x \) or \( W_x \) exceeds a threshold \( \theta \) (as denoted in Algorithms 5 and 6), the first element in \( R_x \) or \( W_x \) is removed.

In this work, we study the basic algorithm of HistLock without the functional context component [21]. We show HistLock’s main algorithms in Algorithms 5 and 6.

III. CASE STUDY

A. Research Questions and Rationales

Section II. \( F-H \) has presented three representative hybrid detectors used in our case study. For ease of presentation, we refer to the (\( \text{epoch} \), lockset) tuple extracted from each memory access event as the context of this memory access. Table I summarizes the design factors of the three detectors.

<table>
<thead>
<tr>
<th>Hybrid Race Detector</th>
<th>Context Sequence Restriction</th>
<th>Context Partitioning Granularity</th>
</tr>
</thead>
<tbody>
<tr>
<td>HistLock</td>
<td>Yes</td>
<td>Coarse</td>
</tr>
<tr>
<td>AccuLock</td>
<td>Yes</td>
<td>Fine</td>
</tr>
<tr>
<td>MultiLock-HB</td>
<td>No</td>
<td>Finest</td>
</tr>
</tbody>
</table>

For each shared memory location, AccuLock just keeps one past write context of any thread and one past read context for each thread. Both MultiLock-HB and HistLock keep multiple contexts rather than one for each type of access of each memory location. MultiLock-HB keeps all the contexts of each access that cannot be determined to be non-interesting. HistLock keeps only some (so long as smaller than the threshold \( \theta \) of these accesses that cannot be determined to be non-interesting. This leads to the formulation of the research question RQ1 below.

RQ1: To what extent can using a sequence of contexts with length restriction detect \( \emptyset \)-races compared to using a sequence of contexts without such restriction?

In the design of HistLock, one write (read, respectively) context sequence is used for each memory location. On the other hand, in the design of MultiLock-HB, a finer granularity is employed in partitioning both read and write context sets for the same memory location (i.e., \( R_x[t] \) and \( W_x[t] \)). Similarly, in the design of AccuLock, although the write context of each memory location can be considered as the first element in a write context sequence, it still uses a criterion of finer granularity to partition the read context set of each memory location (i.e., \( R_x[t] \)). The research question is whether such finer granularity in partitioning contexts makes the detectors with this design less scalable or more scalable than, or as scalable as the ones without the same design. This motivates the following research question RQ2.

RQ2: To what extent does the granularity in partitioning contexts affect the scalability of a detector?

B. Detector Implementation

We have implemented AccuLock (AL), MultiLock-HB (ML), and HistLock (HL) (see Section III. \( F-H \)) in the same framework on Pin 2.10 [12]. Our framework was based on the source code of LOFT [3]. LOFT is a HB-based detector rather than a hybrid detector, so we did not include it in our comparison and data analysis. To detect races, we instrumented synchronization operations (i.e., fork / join, mutex_lock / mutex_unlock, cond_wait / cond_signal, barrier_wait) and memory accesses (i.e., read and write) by inserting callback analysis function probes.

C. Experimental Setup

We selected the PARSEC benchmark suite 3.0 [1] to study the two research questions. This suite is a set of C/C++ multithreaded programs which is also used in previous experiments (e.g., [3][5][10]) on race detection. The benchmark suite contains 13 subjects: blackscholes, bodytrack, canneal, dedup, facesim, ferret, fluidanimate, freqmine, raytrace, streamcluster, swaptions, vips, and x264. The subject freqmine was implemented by OpenMP API [4] and our framework only supported POSIX Threads API. The subject facesim crashed when we ran it under the Pin environment without our framework. We excluded these two subjects from our experiment. We used all the remaining 11 subjects and executed them with the simsmall input test [1]. Table II shows the descriptive statistics of the benchmarks. The threshold \( \theta \) of \( R_x \) or \( W_x \) in HL was configured to \( 4n \), where \( n \) is the default number of worker threads of each subject shipped with the PARSEC suite.

Our experiment was performed on the Ubuntu 12.04 (32bits) Linux with 2.9GHz E5-2690 processor (2 core assigned) and 4GB physical memory managed by Windows Server 2012 Hyper-V Platform. This platform was verified to be able to replicate the empirical result of a hybrid detector reported in the literature.

Using the PARSEC default configuration (see Table II), each benchmark was run 100 times on each detector. We
measured the total number of reported races (the same race is only counted once). A race is defined as the pair of code line generating the events involving in a race warning reported by a detector.

To study the scalability, we repeated the above procedure by systematically varying the number of threads to 4, 8, 16, 32, and then 64. We measured the mean runtime slowdown.

D. Threats to Validity

We did not use the original HistLock [21] in this case study so that we can focus on studying the design factor. The aspect that we have taken out from the original HistLock is the removal of contexts through function invocations. Thus, the interpretation of our empirical results should be careful.

E. Data Analysis

1. Answering RQ1

Table III shows the numbers of data race warnings, along with the number of memory locations instrumented. The column “# of Data Race Warnings” shows the number of races reported by each detector.

As described in Section III.A, we aim to study the effect of context sequence with length restriction (AL and HL) compared to that of context sequence without such length restriction (ML). Thus, we chose ML as a referential φ-race detector. AL and HL reported very similar numbers of race warnings reported by ML on all subjects. We normalize the detection probability of ML to 1. We find that the detection proportion of HL relative to ML is also 1, and that of AL is higher than 0.95. This result shows that using a context sequence with length restriction to keep contexts of selected past access events of each shared memory location could be cost-effective in detecting φ-races. Between the two detectors having context sequence with length restriction, AL missed slightly more φ-races.

2. Answering RQ2

We evaluated the scalability of each race detector by calculating their mean normalized slowdown factor under each fixed numbers of worker threads on all subjects, where the normalized value is the runtime slowdown under each fixed numbers of worker threads over the runtime slowdown under 4 worker threads. The results are shown in Table IV. From the table, the normalized slowdown factor of ML increases dramatically when the number of worker threads grows. On the other hand, only negligible overhead is incurred for the increasing of threads in HL.

<table>
<thead>
<tr>
<th>Subject</th>
<th>Memory Locations</th>
<th># of Reported Race Warnings</th>
</tr>
</thead>
<tbody>
<tr>
<td></td>
<td>w/o length restriction</td>
<td>with length restriction</td>
</tr>
<tr>
<td></td>
<td>ML</td>
<td>AL (+/-)</td>
</tr>
<tr>
<td>blackscholes</td>
<td>53,687</td>
<td>0</td>
</tr>
<tr>
<td>bodytrack</td>
<td>6,121,973</td>
<td>11</td>
</tr>
<tr>
<td>canneal</td>
<td>3,452,210</td>
<td>1</td>
</tr>
<tr>
<td>dedup</td>
<td>18,970,077</td>
<td>0</td>
</tr>
<tr>
<td>ferret</td>
<td>518,308</td>
<td>6</td>
</tr>
<tr>
<td>fluidanimate</td>
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<tr>
<td>streamcluster</td>
<td>167,341</td>
<td>32</td>
</tr>
<tr>
<td>swaptions</td>
<td>550,231</td>
<td>0</td>
</tr>
<tr>
<td>vips</td>
<td>10,742,108</td>
<td>4</td>
</tr>
<tr>
<td>x264</td>
<td>5,713,284</td>
<td>75</td>
</tr>
<tr>
<td>Total</td>
<td>108,070,400</td>
<td>148</td>
</tr>
</tbody>
</table>

<table>
<thead>
<tr>
<th># of Worker Threads</th>
<th>Normalized Slowdown Factor</th>
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<tbody>
<tr>
<td>HL</td>
<td>AL</td>
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<tr>
<td>8</td>
<td>1.00</td>
</tr>
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<td>16</td>
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</tr>
<tr>
<td>32</td>
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</tr>
<tr>
<td>64</td>
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TABLE IV. COMPARISON ON SCALABILITY

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</tbody>
</table>
We plotted the normalized slowdown factor for each detector at subject level, which is shown in Figure 1. From the plots, we observed that HL kept a steady overhead when the number of threads grows. AL had a moderate increment and the mean normalized slowdown factor reached about 1.1 (see Table IV) when the number of threads grew from 4 to 64. The overhead of ML was dramatically influenced by the number of the threads, and the mean normalized slowdown factor reached beyond 1.6 (see Table IV) when the number of threads is 64.

AL employed a fine granularity to partition read contexts belonging to different threads into different equivalent classes, and ML employed an even finer granularity that further partitions both read and write contexts into different equivalent classes by thread. On the contrary, HL does not further partitions both read and write contexts into different equivalent classes by thread. It seems indicating that fine-grained criterion to read or write contexts in the sequences of their corresponding access types. The plots show that the slowdown growth rates are ordered by the granularity of context partitioning, that is, the finer granularity a detector employed on context partitioning, the heavier overhead this detector incurred when the number of worker threads increases. It seems indicating that fine-grained criterion to partition contexts in equivalent classes is inadequate to handle the scalability issue in terms of time cost.

IV. RELATED WORK

Before the inception of FastTrack, there were several hybrid race detectors attempting to combine lockset and happens-before race detection [10][15][16][18][20][24]. After the concept of epoch has introduced in FastTrack, there were several hybrid race detectors employed the idea of epoch along with new lockset algorithm to reduce the false positive or improve the performance of previous hybrid race detectors. We have reviewed AccuLock [19][20], MultiLock-HB [20] and HistLock [21] in the case study reported in this paper.

SimpleLock [22] and SimpleLock+ [23] are interesting attempts which aim at lowering the overhead incurred by epoch-based hybrid detectors at the expense of soundness. Compared to MultiLock-HB [20], SimpleLock [22] only keeps the number of locks being held by a thread at an epoch rather than the identity of each of those locks. It is efficient, but cannot be generalized to detect LD violations involving different locks protecting the same memory location. SimpleLock+ [23] simplifies SimpleLock [22] by recording both the latest memory access of each thread and whether the memory location has a lock to protect at each epoch only. SimpleLock+ is more lightweight than SimpleLock, but it does not address the soundness problem incurred by SimpleLock. Because both SimpleLock and SimpleLock+ are unsound, we exclude them from our case study.

V. CONCLUSION

This paper has reported a case study to investigate the design factor in achieving highly effective race detection through context sequence restriction and its context partitioning granularity. The preliminary results have shown that context sequences with length restriction are effective in detect a high proportion of LD violations detected through using context sequences without such length restriction, and the partitioning of context sets into finer equivalent classes significantly lowers the scalability in time cost with increasing number of threads to handle the same input workload.

REFERENCES